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THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Applicant:

Tyler A. Lowrey et al.

Serial No.: 10/633,872

Filed: August 4, 2003

For: Read Bias Scheme for
Phase Change Memories

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Art Unit: 2827

Examiner: Thong Quoc Le

Docket: ITO.0046US
P16201

Assignee: Intel Corporation

Mail Stop **Amendment**
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

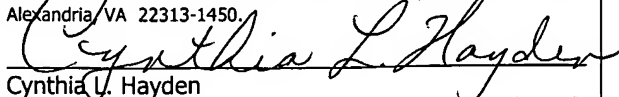
REPLY TO PAPER NO. 4

Sir:

In response to the office action mailed September 26, 2005, please amend the above-referenced patent application as follows:

Date of Deposit: December 2, 2005

I hereby certify under 37 CFR 1.8(a) that this correspondence is being deposited with the United States Postal Service as **first class mail** with sufficient postage on the date indicated above and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.


Cynthia L. Hayden